

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Memory Module with Dynamic Termination Using Bus Switches Timed by Memory Clock and Chip Select
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Application Number :

10710475

Confirmation Number:

First Named Applicant: Yao Yen

Attorney Docket Number: PS-122

Art Unit:

Examiner:

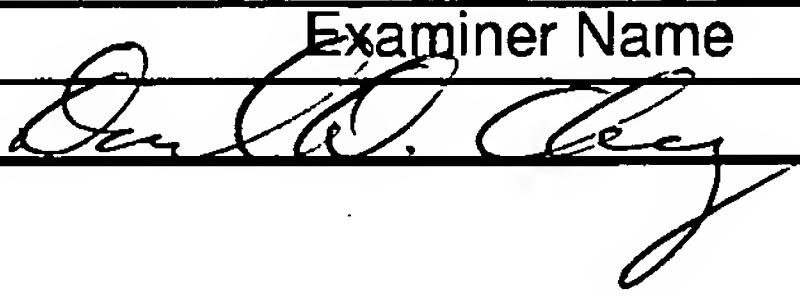
Search string: (6738844 or 6714465 or 6686763 or 6538951 or 6429678 or 6356106 or 6266252 or 6072342 or 5729152 or 5227677).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
dc	1	6738844	2004-05-18	Muljono , et al.	B1	710	107
dc	2	6714465	2004-03-30	Jang	B1	365	194
dc	3	6686763	2004-02-03	Yen	B1	326	30
dc	4	6538951	2003-03-25	Janzen , et al.	B1	365	230.03
dc	5	6429678	2002-08-06	Wong , et al.	B1	326	30
dc	6	6356106	2002-03-12	Greeff , et al.	B1	326	30
dc	7	6266252	2001-07-24	Karabatsos	B1	361	788
dc	8	6072342	2000-06-06	Haider , et al.	B1	327	112
dc	9	5729152	1998-03-17	Leung , et al.	B1	326	21
dc	10	5227677	1993-07-13	Furman	B1	326	21

Signature

Examiner Name	Date
	1/24/06